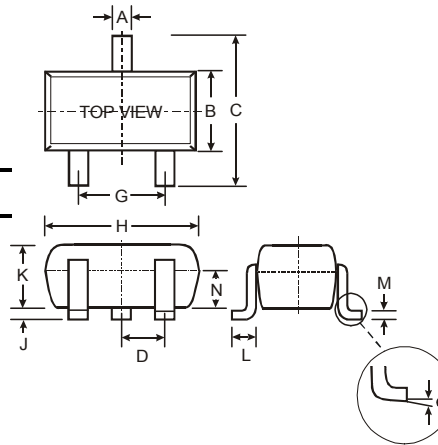


Features

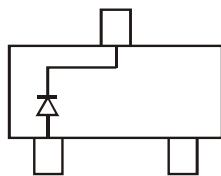
- Ultra-Small Surface Mount Package
- Low Forward Voltage Drop
- Fast Switching
- PN Junction Guard Ring for Transient and ESD Protection
- **Lead Free/RoHS Compliant (Note 3)**
- **"Green" Device (Note 4 and 5)**

Mechanical Data

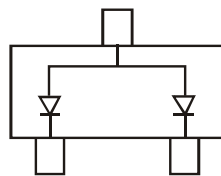
- Case: SOT-523
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish - Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Polarity: See Diagrams Below
- Marking: See Diagrams Below & Page 3
- Ordering Information, see Page 3
- Weight: 0.002 grams (approximate)



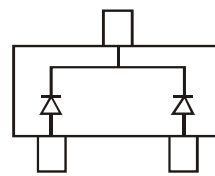
SOT-523			
Dim	Min	Max	Typ
A	0.15	0.30	0.22
B	0.75	0.85	0.80
C	1.45	1.75	1.60
D	—	—	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
J	0.00	0.10	0.05
K	0.60	0.80	0.75
L	0.10	0.30	0.22
M	0.10	0.20	0.12
N	0.45	0.65	0.50
α	0°	8°	—
All Dimensions in mm			



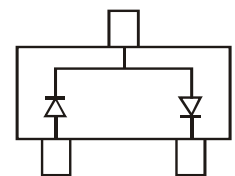
BAT54T Marking: L1



BAT54AT Marking: L2



BAT54CT Marking: L3



BAT54ST Marking: L4

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	30	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
Forward Continuous Current (Note 1)	I _{FM}	200	mA
Repetitive Peak Forward Current	I _{FRM}	300	mA
Forward Surge Current @ t < 1.0s	I _{FSM}	600	mA
Power Dissipation (Note 1)	P _d	150	mW
Thermal Resistance, Junction to Ambient (Note 1)	R _{θJA}	833	°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +125	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	V _{(BR)R}	30	—	—	V	I _R = 100μA
Forward Voltage	V _F	—	—	240	mV	I _F = 0.1mA
				320		I _F = 1mA
				400		I _F = 10mA
				500		I _F = 30mA
				1000		I _F = 100mA
Reverse Leakage Current (Note 2)	I _R	—	—	2.0	μA	V _R = 25V
Total Capacitance	C _T	—	—	10	pF	V _R = 1.0V, f = 1.0MHz
Reverse Recovery Time	t _{rr}	—	—	5.0	ns	I _F = 10mA through I _R = 10mA to I _R = 1.0mA, R _L = 100Ω

- Notes:
1. Device mounted on FR-4 PC board with recommended pad layout, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
 2. Short duration pulse test used to minimize self-heating effect.
 3. No purposefully added lead.
 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 5. Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

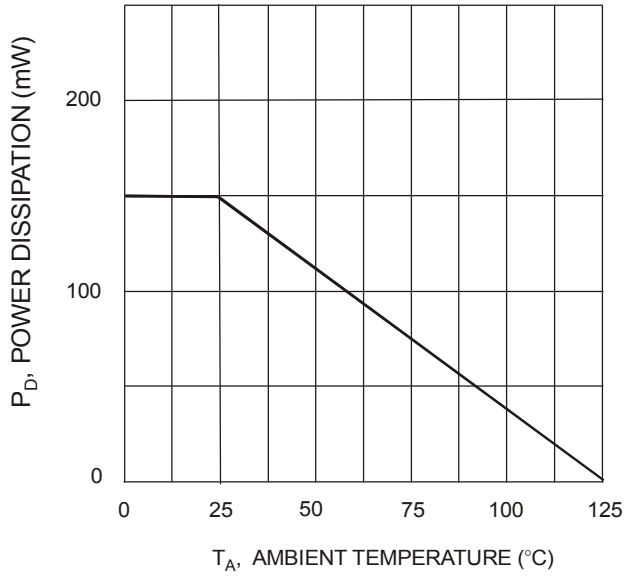


Fig. 1 Power Derating Curve

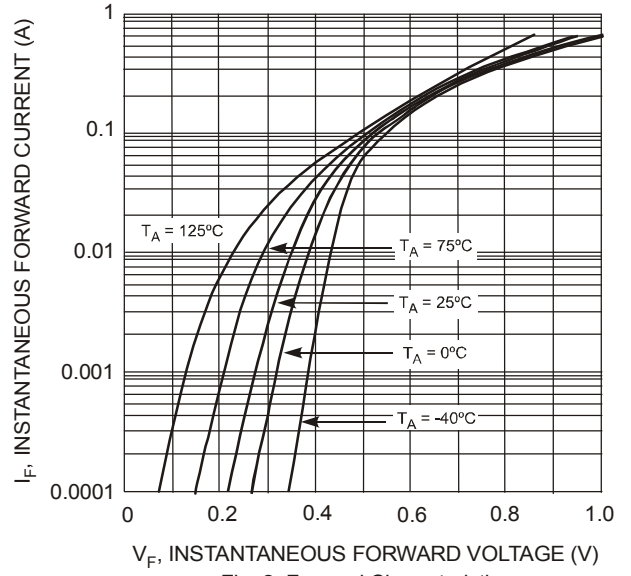


Fig. 2 Forward Characteristics

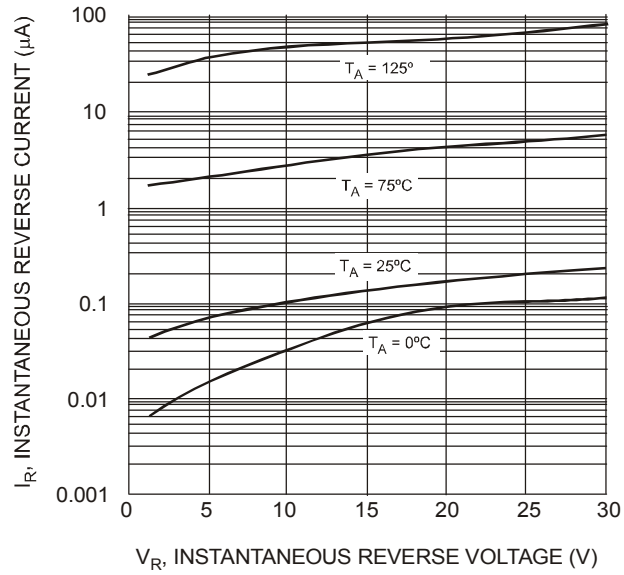


Fig. 3 Typical Reverse Characteristics

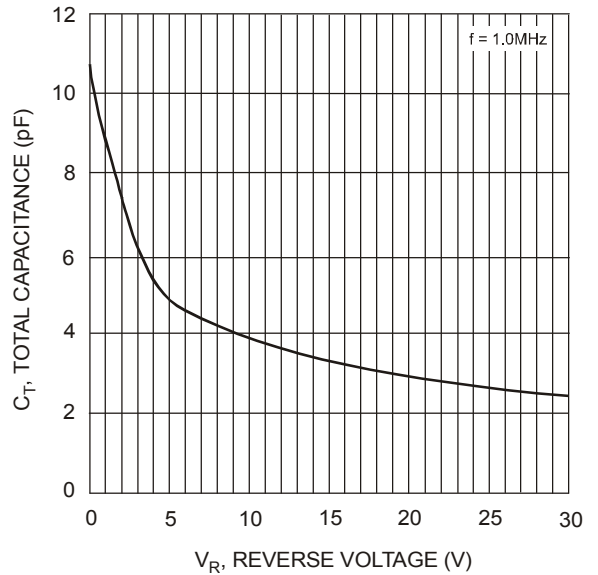


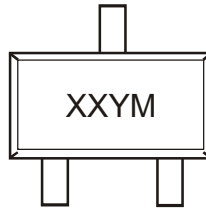
Fig. 4 Typical Capacitance vs. Reverse Voltage

Ordering Information (Note 6)

Device	Packaging	Shipping
BAT54T-7-F	SOT-523	3000/Tape & Reel
BAT54AT-7-F	SOT-523	3000/Tape & Reel
BAT54CT-7-F	SOT-523	3000/Tape & Reel
BAT54ST-7-F	SOT-523	3000/Tape & Reel

Notes: 6. For packaging details; go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



XX = Product Type Marking Code (See Page 1, e.g. L1 = BAT54T)
 YM = Date Code Marking
 Y = Year (ex: N = 2002)
 M = Month (ex: 9 = September)

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	N	P	R	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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